

## Patent Assignment Abstract of Title

**Total Assignments: 1**

**Application #:** 09716721    **Filing Dt:** 11/20/2000    **Patent #:** NONE    **Issue Dt:**  
**PCT #:** NONE    **Publication #:** NONE    **Pub Dt:**

**Inventors:** Thomas Edward Horlander, Eric Stephen Carlsgaard

**Title:** Serial compressed bus interface having a reduced pin count

**Assignment: 1**

<b>Reel/Frame:</b>	<u>011586 /</u> <u>0626</u>	<b>Received:</b>	<b>Recorded:</b>	<b>Mailed:</b>	<b>Pages:</b>
		05/21/2001	05/21/2001	05/21/2001	2

**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

**Assignors:** HORLANDER, THOMAS EDWARD

**Exec Dt:** 04/17/2001

CARLSGAARD, ERIC STEPHEN

**Exec Dt:** 04/09/2001

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2 INDEPENDENCE WAY

PRINCETON, NJ 08543-5312

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<input type="checkbox"/>	L4	(serial-to-parallel or (serial near1 to near1 parallel)) near1 (converter\$ or convert\$6 or conv\$8)	5286
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